

Electronic Acknowledgement Receipt

EFS ID:	1525355
Application Number:	10091934
International Application Number:	
Confirmation Number:	7058
Title of Invention:	Interconnect-aware methodology for integrated circuit design
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Filer:	Suzanne Erez/Imogene Friedman
Filer Authorized By:	Suzanne Erez
Attorney Docket Number:	IL920020007US1
Receipt Date:	19-FEB-2007
Filing Date:	06-MAR-2002
Time Stamp:	07:25:08
Application Type:	Utility

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)		
1		IL920020007US1_RESRR_3 0JAN07.pdf	24556	yes	3		
	Multipart Description/PDF files in .zip description						
	Document Description	Start	End				
	Response to Election / Restriction Filed	1	1				
	Claims	2	2				
	Applicant Arguments/Remarks Made in an Amendment	3	3				
Warnings:							
Information:							
Total Files Size (in bytes):			24556				
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